



Seasonal School on Circuits and Systems: Analog Integrated Circuit Design with EDA Tools

Date:
8-9 November, 2016

Time:
09:00 to 16:00

Location:
Hotel Wyndham-
Herradura, Alajuela,
Costa Rica

Space is limited
To reserve your seat,
please RSVP to:
renato.rimolo@ieee.org

This 2-day school covers the custom integrated circuit design flow with Synopsys EDA tools. The course is imparted by Synopsys trainers and it includes lectures with quizzes to test for understanding, as well as challenging labs.

Topics:

- Introduction to Analog CAD
- Basic Amplifier Design
- Differential Circuits
- Subcircuits
- Layout Techniques
- Analog design using Synopsys tools (w/labs)

Instructors: Victor Grimblatt, Ronald Valenzuela, Esteban Viveros, Synopsys, R&D Center Chile.

Investment: \$300, IEEE Members \$200, IEEE-CASS \$150, IEEE Students \$100, IEEE-CAS Students \$30.

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