

# **IEEE Sweden Solid-State Circuits/Circuits and Systems Joint Chapter**

Ted Johansson, vice chair  
Linköping university

# IEEE Sweden SSC/CAS

- Around 370 members. 2003/2013
- Previously arranged local conference SSoCC for many years.
- Decline in (traditional) circuit design activities in Sweden, incl. funding for PhD positions.
- No SSoCC conferences 2016, 2017
- Much money in pocket from previous conference
- Sponsor of WEEE17' international workshop 2017, Ystad
- Sponsor of NORCAS 17, Linköping
- SSCS Chapter chair meeting at ESSCIRC 2017, Leuven

The screenshot shows the website for the IEEE Sweden Solid-State Circuits/Circuits & Systems Joint Chapter. The page title is "IEEE Sweden Solid-State Circuits/Circuits & Systems Joint Chapter". The navigation menu includes "Home", "Board", "Reports and Minutes", "Events", "Links", and "SSoCC". A search bar is located below the navigation menu. The main content area is titled "SSoCC" and contains the following information:

**SSoCC**

- SSoCC15: photo gallery
- SSoCC14: photo gallery
- SSoCC13: photo gallery
- SSoCC11: photo gallery
- SSoCC10: photo gallery
- SSoCC09: photo gallery
- SSoCC08: photo gallery

**Home > SSoCC**

**SSoCC**

The Swedish System-on-Chip Conference (SSoCC) is arranged each year by IEEE Sweden Solid-State Circuits/Circuits & Systems Joint Chapter.

**SSoCC 2015**

- Conference opening slides (TBD)
- [Konferensrapport Elektronik i Norden 2015-05-19](#)
- [Kortare konferensrapport Chalmers](#)

**SSoCC 2014**

- [Conference opening slides \(pdf\)](#)

**SSoCC 2013**

- [Conference opening slides \(pdf\)](#)

**SSoCC 2011**

- [Konferensrapport Elektronik i Norden 5/2011](#)
- [Conference report SSC Magazine 4/2011](#)

**SSoCC 2010**

- [Konferensrapport Elektronik i Norden 4/2010](#)
- [Conference report SSC Magazine 4/2010.pdf](#)

**SSoCC 2009**

- [Conference report SSC Magazine 4/2009.pdf](#)

# Main sponsor of WEEE, Ystad Sweden



**WEEE17'**

12-14 June 2017  
Lund University, Sweden

Workshop on  
Energy Efficient Electronics and  
Applications and SOI Training



- 1.5 day workshop. Previously held in Israel and Lausanne.

The objective of this workshop is to bring together experts, from both industry and academia, in system architecture, circuit design, and technology to discuss the challenges and the latest trends in the development of low-power and ultra-low-power embedded systems.

\* Low-power embedded systems and computing architectures

\* Ultra-low power and sub-threshold circuit design

\* Variability and reliability issues in modern process technologies and corresponding new design paradigms

\* Advanced CMOS and More-than-Moore Technologies for mobile, wearable, and embedded applications.

Speakers from Intel, STM, ARM, Minimaprocessor (FI), KU Leuven, IMEC, RWTH Aachen, Lund U, U Bologna, EPFL Lausanne, Bar-Ilan University Israel.

Around 60 participants.

IEEE SSC/CAS sponsoring of traveling costs for speakers.

- 1.5 day Cadence training for 28 nm FD-SOI design tools, at Lund University. Around 30 participants. Sponsored fully by Cadence.

# 2017 NorCAS Conference

## IEEE Nordic Circuits and Systems Conference

24-25 October 2017, Linköping, Sweden

The IEEE NORCAS conference is the main circuits and systems event of the Nordic countries representing both academia as well as electronics industry.

The new IEEE NORCAS conference is a merge of the well-established conferences NORCHIP and SoC. The annual IEEE CAS sponsored conference covers all fields of electronics design, spanning from complex digital systems to advanced analog and mixed-mode circuits.

The wide scope of the conference promotes cross-field collaboration not only between academics both also with industry.

Papers of high scientific and technical quality are presented together with selected invited speakers and pre-conference tutorial sessions.

