State of Pacific Rim Nanotech., specially in Asia

- Green Nanotechnology -

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Outline

1. Introduction
2. Statistics
3. Nano-carbon
4. Energy devices with nanomaterials
5. Si Photonics
Introduction
Explosion of Information Traffic and Electric Power Consumption of ICT System

- Electric power consumption at data-centers in US was doubled in the past 6 years, reaching to the electric power generated by 5 nuclear power plants.
- Information traffic in the Internet will increase 190 times from 637Gbps@2007 to 120Tbps@2025.
- Already, the power consumption of IT system at 2007 is five times larger than that at 2006. The reduction of power consumption must be one of the very urgent issues.

Change of power consumption at data-centers in US from 2000 to 2006
Change of power consumption of IT system with years

Source: METI “Green IT Initiative”
To realize sustainable society

- Energy and environmental issues have to be solved to realize sustainable society.
- Many technologies in broad area have to contribute to do so.
- Nanotechnology is one of the most important technologies.
NEC’s Nanotech. Development Strategy

Nanotechnology supporting to realize information society friendly to humans and the earth

Information society friendly to humans and the earth

Nanotechnology

friendly to humans

friendly to the earth

Physics and Materials

- Quantum Effect (Wave Nature of Particle)
- Near Field (Trapped Light)
- Self-Organization (Automatic Formation)
- Ballistic Effect (High-speed movement)
- Surface Effect (Lower Melting Point / High Reactivity)

- Eco-material
  - Energy and resource saving production
  - Bio-plastics

- Nano-bridge
- Si photonics
- CNT printed transistor

- High performance Information processing and ubiquitous safety

- Ubiquitous Battery

- Nano-simulation

- Bit/ECO value by using nanotechnology
Statistics
Share of nanotechnology-related and all publications by country, 1991-2007

Share of nanotechnology-related and all publications of China is larger than that of Japan. 1. USA 2. China, 3. Japan, 4. Germany, 5. France
Average annual growth rates of nanotechnology-related and all publications by country, 1996-2006

Nanotec. Publications from Asian countries (Singapore, China, Korea, Chinese Taipei) have grown.
Share of nanotechnology and all patents by country, until 2005

The number of nanotech. patents of USA is half of all.
Average annual growth of nanotechnology and all patents by country, 1995-2004

The number of patents of Asian countries (Korea, India, China, Japan) has grown.
Nanocarbon
Focused Applications: energy and display devices
1. Battery/Capacitor with use of large surface area
2. Transparent electrode alternative to ITO
3. Printable electronics

Key Technologies
1. Controlled Growth:
   • Diameter (and chirality) control for nanotube
   • Minimized defect density for graphene
2. Large Scale Fabrication:
   • CVD technology with suitable substrates/catalysis
   • Manipulation/transfer to arbitrary substrates
AIST Japan demonstrated a method of diameter control by tuning the size of catalysis particle. Verified by optical absorption spectroscopy of grown nanotubes.

Millimeter sized super-growth of CNT (AIST)

The large scale growth technique of carbon nanotubes, so called “super-growth technique”, opened a door of applications of macroscopic sizes of nanotube material.

Application example 1: Actuator with significant toughness

Electrolyte actuator is operable in air being sandwiched by SG-CNT electrodes.

Application example 2: Super-capacitor with Large surface area CNT material as possible hybrid power-source with Li ion battery

http://www.nanocarbon.jp/sg/001.html
Large area CVD growth of graphene on Ni/Cu foil and manipulation (Sungkyunkwan Univ. Samsung, Korea)

Sequence of CVD growth of graphene on Ni or Cu substrate, and subsequent transport process on arbitrary substrate are developed.

Possible future application: Transparent electrode for wearable touch-panel displays tougher than ITO against bending

Recently this technique is scaled up to A4 size of graphene sheet with roll-to-roll printing.

http://chem.skku.edu/graphene/
Printed CNT transistor (NEC)

- All parts of transistors (channel/insulator/electrodes) are printed.
- Process temperatures are below 200 degree C, applicable for plastic film.
- We succeed to fabricate printed CNT transistors, which on/off ratio is >1000.

Fabrication process

CNT transistor on the flexible substrate

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Carbon Nanotubes as printed electronic materials

Structural characteristics
- Chemically stable
- Mechanically stable

Electronic Characteristics
- Large carrier mobility
- Large current capacity
- Suitable for making ink
- High-speed operation
- Handled in humid condition
- No degradation in solvent
Energy Devices
Trend for Rechargeable Batteries

**EV mass production will lead the market expansion of Energy Devices**

- **1990~** Mobile devices
- **2010~** EV, HEV
- **2012~** LiB Market expansion
- **Transportation**
  - Bus
  - Train
  - Aircraft
- **Construction Equipments**
  - Forklift
- **Stationary Battery**
  - Mega Solar
- **Ubiquitous Devices**
  - Flexible device
  - Wearable device
  - E-book
- **E-book**
- **Wearable device**
- **Flexible device**
- **Stationary Battery**
- **Mega Solar**
- **Wind Power**
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  - **Background:** World’s primary energy consumption
  - Global warming
  - Oil shortage
  - Air pollutions
  - Environment destructions

**Graph:**
- **Primary Energy Consumption / M TDE**
- **Year:** 65, 70, 75, 80, 85, 90, 95, 00, 05
- Regions:
  - North America
  - Central-South America
  - Europe
  - Africa
  - Asia, pacific

http://www.bp.com/
Advanced Energy Storage Devices

Breakthrough technologies are required for higher energy and power density.

Conventional LiB

**Technical Goals/Metrics**
- Cells for portable devices
- High power electrode
- Long life; stability during cycling
- Low cost operation

**Technical Challenges**
- Nano storage materials/structures
- Reliability, cost, lifetime, durability
- Temperature range, safety
- Manufacturing technologies

**Nanomaterials**

- Power: shortened $e^{-}$ path length
- Rate property: increase contact area
- Energy density: high surface area

Nanomaterials chemistry is essential for the future challenges of energy storage devices.
High-power Cathode Study (Nagasaki Univ. & AIST)

Nano-composite LiMnPO$_4$-C as cathode by Dr. Honma and Prof. Moriguchi.

Technical Accomplishments
- High power cathode
  => Rate capability x2.8
- High energy density; x2 to bulk
- Safety; low toxic materials
- Low cost (Mn)

Technical Approach
- Nano particle LiMnPO$_4$ (<200 nm)
- Olivine composite with C in surface is formed successfully
- Composite interface enhances electron conductivity

"Fast Li-ion insertion into nanosized LiMn2O4 without domain boundaries"
Highly-structured Alloy as Anode (Tokyo Metro. Univ.)

Lithium ion battery with 3DOM* Sn-Ni alloy as anode by Prof. Kanamura.

*3 Dimensional Ordered Marcoporous

Technical Accomplishments
- Potentiality of Sn-based electrode has been shown
- High energy density; 993 mAh/g(ideal) >500mAh/g(current), x1.5 to graphite
- 60%@60Cycling; Long life as Sn-based material
- 99% of charge-discharge efficiency

Technical Approach
- Colloidal particles as template for 3DOM structure
- Relaxation of mechanical stress by volume expansion

J. Hamagami, K. Hasegawa, K. Kanamura
“3D particle assembly in micro-scale by using electrophoretic micro-fabrication technique”
Organic Radical Battery (NEC)

A new class of rechargeable battery using flexible plastic by NEC.

Technical Accomplishments
- Flexible/Thin (<1 mm) battery: reduce volume, free form
- ECO-friendly: no harmful metals such as Pb, Sn, Co, etc
- High power: 6.7 kWh/L
- Rapid recharging

Technical Approach
- Nitroxy-radical polymer (PTMA); as a charge storage material
- Lithium-ion operating battery
- Nano-carbon fiber as conductivity path in composites cathode

Energy density depends on fiber diameter

Organic Radical Battery (2) (NEC)

Application images of micro-power battery for ubiquitous/ sensor-NW devices.

Wearable device

Active cards

Communication cards (Wireless charging)

USB charger

Small Organic radical batteries would be possible solution for man-wearable power such as activation cards/tags
Si photonics
Optical Interconnects in Electronics

- Optical communication has been employed to long haul telecommunication for internet progress.
- High data capacity optical communication has been penetrating from long-haul to short-distance interconnect. Also, market will be increased in short distance interconnect.

Telecommunication

Other distances:
- Between Computers: km
- Between Boards: m
- Between Chips: cm

Smaller Distance

Larger Market

On-Chip Interconnect
In 2012, a 1-mm-long interconnect’s latency will be 100 times larger, and its binary switching energy will be 30 times larger, than a corresponding transistor.


**3D-Integration**

**Optical Interconnect**

- **Shorter Length by Vertical Connection**
- **Different Physics for Lateral Connection**

Si Interposer

TV TEG #1

TV TEG #2

TV TEG #3

Bumped TEG

20 µm pitch Cu Bump

Cu Thru Via

Cross-section

10 µm

Optical Signal

SiON waveguide

Nano photodiode
Power Consumption in LSI

Dynamic power will grow larger than maximum heat removal capability. Especially, interconnect have large proportion according to progress of process technology node. This means interconnect will be very important for progress of LSI.
Electrical interconnect, especially global wiring, have to use many repeaters for escaping delay at long interconnect and also at high speed.

20 mm-interconnects with repeaters

M. Mizuno et al., ISSCC 2001
Electrical Interconnect

Cu/Low-k, Repeaters

High Power Consumption by Interconnect

<table>
<thead>
<tr>
<th>Operation</th>
<th>Energy (130 nm, 1.2 V)</th>
</tr>
</thead>
<tbody>
<tr>
<td>32-bit ALU operation</td>
<td>5 pJ</td>
</tr>
<tr>
<td>32-bit register read</td>
<td>10 pJ</td>
</tr>
<tr>
<td>Read 32 bits from 8K RAM</td>
<td>50 pJ</td>
</tr>
<tr>
<td>Move 32 bits across 10 mm chip</td>
<td>100 pJ</td>
</tr>
<tr>
<td>Move 32 bits off chip</td>
<td>1300 to 1900 pJ</td>
</tr>
</tbody>
</table>

Bill Dally, ISSCC 2005
From Electrical To Optical

Advantages: No repeaters, Small delay, Small jitter, EMI, High data capacity, …

Issues: Power consumption overhead of EO/OE conversion
Silicon Photonics activities in Asia

Si photonics are investigated by using 6 & 8-inch CMOS Lines in Singapore and Shanghai.

- **Institute of Microelectronics, Singapore**

Si modulators and Ge detectors are monolithically integrated. Si modulator achieves 10Gbps operation at 2-mm-long device.

- **Shanghai Institute of Microsystem and Information Technology (SIMIT) & Institute of Semiconductor, Chinese Academy of Sciences**

Si modulator achieves 10Gbps operation.

http://www.sim.cas.cn/xwzx/kyjz/201003/t20100323_2804854.html
Small, High-Efficiency and High-Speed Mach-Zehnder-Type MOS-structured Silicon Modulators

Silicon MOS (metal-oxide-semiconductor) optical modulator that boasts a leading power-consumption efficiency of less than 1mW/Gbps, a compact size of 120-μm in length and high-speed operation of 25Gbps.

Benchmark of Mach-Zehnder-type Silicon Modulators

25Gbps Eye diagrams
Si nanophotodiode with surface plasmon antenna

- Ag plasmon antenna assists effective absorption of light in Si from SiON waveguide
- Ultra-small (<10μm), High-speed (>50GHz) & High-sensitivity PD by novel surface-plasmon effects

Fastest response in Si-PDs (>50GHz)
Summary

- Nanotechnologies are very important to realize the future sustainable society.
- Nanocarbon, such as CNT and graphene, electronics are useful for printable electronics, large-area electronics.
- Nano-material technologies are essential to improve the capacity, stability and reliability for energy and environmental devices.
- Si photonics is one of the key technologies for interconnections in the future IT/NW systems.
Empowered by Innovation

NEC