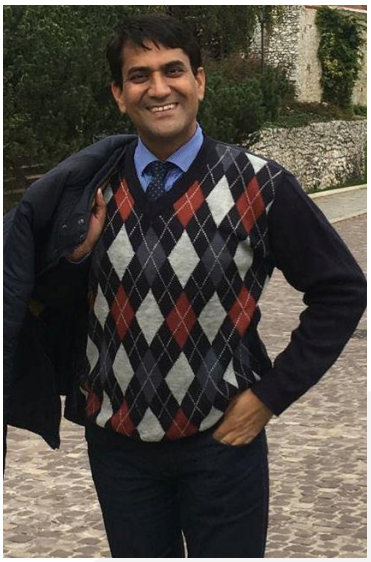
Photonic Integrated Devices and Systems:
Technology for next Generation Telecom Networks

Politecnico di Torino
Maxwell Room (DET 5th floor)
May 13, 2018 - 15.00

FREE ENTRY

IEEE Photonics Society
Distinguished Lecture



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Abstract

The information explosion can be dealt with, using integration of very small photonic components on very high density Photonic Integrated Circuits (PICs).
The technological advancements in PICs have made them a popular choice for components of next generation networks. Silicon being the evident choice due to its high availability, mature fabrication technology, and low cost has attracted the researchers to explore the possibilities of integrating the fast photonics components on a chip. Due to robustness, flexibility, reliability and performance of PICs, many commercial solutions are now available for avariety of applications. In coming years, it is expected that the field will continue to advance and communicationnetworks may see a shift from electronic to all-optical/electro-optic network infrastructure.

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