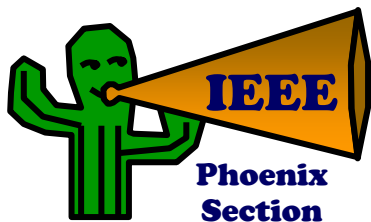


The Valley Megaphone

Newsletter of the
**Institute of Electrical and
Electronics Engineers, Inc.,
Phoenix Section**
November 2014,
Volume XXVIII, Number 11



Executive Committee 2014

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kbharath@ieee.org

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IEEE Phoenix Section on-line updates can be found at <http://sites.ieee.org/phoenix/> and on LinkedIn at: <http://www.linkedin.com/groups?gid=2765918> and on Facebook at: <https://www.facebook.com/IEEEPhoenixSection>

Please send announcements for the *Valley Megaphone* to Mahesh Shah at mkshah@ieee.org for inclusion in the Section Calendar.

All meetings announced in the Phoenix Section Megaphone or on the Phoenix Section Calendar are open to everyone (IEEE members and non-Members)

Chapters

Signal Processing & Communications

Pavan Turaga
pturaga@asu.edu

Computer Society

Jerry Crow
jerry.crow@computer.org

CPMT Society

Mahesh Shah
480-544-9438
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Education Chapter

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EMBS Chapter

TBD

EMC Society

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Power & Energy Society

Craig Smith
craig.smith@aps.com

Solid State Circuits

Mirembe Musisi-Nkambwe
Mirembe@ieee.org

Teacher-In-Service

Rickie Currens
Rickie.Currens@att.net

Waves & Devices Society

Steve Rockwell
steve.rockwell@ieee.org

Life Members

Les Daviet II
lesdavietii@cs.com

Women In Engineering

Shamala Chickamenahalli
shamala.chickamenahalli@intel.com

Young Professionals

Shafiul "Jacky" Islam 520-245-9010
shafiul.islam@intel.com

U – News

(for Student Members)

Updates of Student Advisors and Committee Members

Each Student Branch noted on the right side of this page should review current information on Advisors and Student Committee Members and forward to my attention within this week, as we are reviewing contacts for reporting and activities including Student Monthly Meetings.

S. Diane Smith
602-749-4601
sdianesmith@computer.org
Student Activities Chair

Student Branches

ASU Main, Engineering

Chair: Nick Spirakus
480-789-9867,
nmspirak@asu.edu
Advisor: Cihan Tepedelenioglu,
480-965-6623, cihan@asu.edu

ASU Main, Computer Society

Chair: TBD
Advisor: Guoliang Xue
480-965-6218, xue@asu.edu

ASU Polytechnic

Chair: TBD
Advisor: TBD

DeVry, Phoenix

Chair: Lori Renaldi
lorirenaldi@computer.org
Advisor: Diane Smith
dsmith2@devry.edu

DeVry, Computer Society

Chair: TBD
Advisor: Diane Smith
dsmith2@devry.edu

NAU, Engineering

Chair: TBD
Advisor: Niranjan Venkatraman
v.niranjan@ieee.org

Embry-Riddle, Prescott

Chair: Lisa M. Ferguson
FERGUSL2@my.erau.edu
Advisor: John E. Post
posti@erau.edu

U – Newsbytes

ASU Polytechnic is currently seeking Advisor for the Student Branch. Please email Diane (at email address above) with Recommendations.

The Department of Computer, Electrical, and Software Engineering at **Embry-Riddle Aeronautical University** in Prescott, AZ invites applications for a faculty position (tenure-track or tenured) at the assistant or associate professor level. The successful candidate should have a BS degree in Electrical or Computer Engineering, and either an earned doctorate in Electrical or Computer Engineering, or a MS degree in Electrical or Computer Engineering, or equivalent, and extensive industrial experience with wireless or wireline communications, analog or power electronics, navigation systems, or avionics systems. Preferred areas of expertise for this position include extensive knowledge of electronic and wireless (both analog and digital) communications systems theory and practice as well as the ability to develop and manage senior design projects in this area. Significant industrial, entrepreneurial and/or teaching experience would strengthen a candidate's credentials, but recent graduates are encouraged to apply.

Apply online at www.erau.edu/jobs and search for positions at Prescott.
Contact Professor Cone at conec@erau.edu for further information.



Upcoming Conferences in Region 6

[2015 IEEE/MTT-S International Microwave Symposium - MTT 2015](#) Will be held May 16 to 22 2015

[2015 International Conference on Microelectronic Test Structures \(ICMTS\)](#) will be held March 23 to 26 2015

[2015 85th ARFTG Microwave Measurement Conference \(ARFTG\)](#) will be held on May 22, 2015

[2015 IEEE International Conference on Cloud Engineering \(IC2E\)](#) will be held March 9 to 13

[2015 IEEE Workshop on Automatic Speech Recognition and Understanding \(ASRU\)](#) will be held Dec 13 to 16, 2015

Metro Area Workshop

We are trying to plan an all day workshop here in Phoenix for the spring of 2015. It will be for learning and networking and will be kept affordable so that everyone who wants to attend can. We are planning on having a keynote speaker and then break into a number of classes all day, with breaks for snacks, lunch, and networking.

Anyone who would like to volunteer to help or is able to teach a class, please contact me at Phoenix.conferences@ieee.org or bradscientist@ieee.org

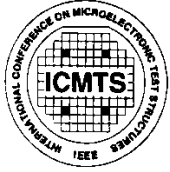
We are also planning on having exhibits for the attendees to learn about new companies, products, and to meet other people. The booths will be reasonably priced, and if anyone is interested, please contact me at the email listed in the lines above.



Call for Papers

28th International Conference on Microelectronic Test Structures

March 23-26, 2015, Phoenix, Arizona USA



General Chair:

Larg Weiland
PDF Solutions
larg.weiland@pdf.com

Technical Chair:

Colin McAndrew
Freescale Semiconductor, Inc.
Colin.McAndrew@freescale.com

Tutorial Chair:

Brad Smith
Freescale Semiconductor, Inc.
Brad.Smith@Freescale.com

Local Arrangements:

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Freescale Semiconductor, Inc.
Colin.McAndrew@freescale.com

Equipment Exhibition:

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Agilent/Keysight Technologies
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Asian Representative:

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VLSI Design and Education Center
University of Tokyo
asada@silicon.u-tokyo.ac.jp

European Representative:

Anthony Walton
Scottish Microelectronic Centre
University of Edinburgh
Anthony.Walton@ee.ed.ac.uk

USA Representative:

Loren Linholm
linhlw@comcast.net

Conference Manager:

Wendy Walker
Widerkehr and Associates
wwalker@widerkehr.com

The 28th International Conference on Microelectronic Test Structures (ICMTS) will be held in Phoenix, Arizona, USA, bringing together designers and users of test structures to discuss recent developments and future directions. The conference will be held March 24-26, 2015, preceded by a one-day Tutorial Short Course on Microelectronic Test Structures on March 23. There will be an equipment exhibition relating to test structures and measurements. Original papers are solicited presenting new developments in test structures, as well as their implementation, measurement, and application, related to semiconductors, nanotechnology, and MEMS. A Best Paper award will be presented by the Technical Program Committee. The conference is sponsored by the IEEE Electron Devices Society and all published papers will be posted to IEEE Xplore®.

Topics of relevance to ICMTS include, but are not limited to:

Material and Process Characterization: Wafer material evaluation for SiGe, strained Si, Si-on-insulator, Ge, GaAs, GaN and other compounds. Resistivity, mobility, stress, contact resistance, dielectric, and interconnect measurements. Test structures and methods to evaluate new materials and devices, e.g. graphene and CNTs.

Test structure design methods: Flows for automated test structure design, generation, and verification; design-for-analysis, parameterized design, layout issues (grid, hierarchy, misalignment), switched arrays.

Replicated Feature Metrology: Level-to-level registration, overlay, CD uniformity and control, non-electrical characterization techniques, mask and reticle process control.

Manufacturing of Integrated Circuits and MEMS: Evaluation of individual and groups of integrated circuit, device, and MEMS process steps and elements: transistors, diodes, mechanical structures, device isolation, memory cells, and interconnect. Assessment of MMICs and RF components and products. Evaluation and optimization of standard cell macros and other circuits.

Reliability and Product Failure Analysis: Test structures for quality assurance, transistor, thin film, dielectric, and interconnect reliability, thermal monitoring and analysis, accelerated wafer level tests, wafer level burn-in, failure identification, reliability prediction.

Nanotechnology, Displays, and Emerging Devices: Test structures and methods to evaluate nanotechnology (materials and devices), displays, optoelectronic materials and devices, novel memories, and related materials.

(BIO-)MEMS, (BIO-)Sensors, and Actuators: Test structures for MEMS and micromachining including physical/chemical/optical/bio sensors, photonic devices, amorphous silicon films and devices.

Device and Circuit Modeling, Parameter Extraction: Model parameter extraction, RF device modeling, de-embedding, pulsed measurements, DC and high frequency measurement techniques and applications.

Technology R&D, Integration, and DFM: Test structures for FEOL or BEOL evaluation, design rule determination, process uniformity and worst-case analysis, test structures to assess integration and new technologies, switched array test chips/devices for large scale evaluations and reduced pad count.

Test Circuits: Novel on-wafer circuits for characterization of manufacturing technologies, variability, yield, and performance. Circuits to simplify probing, improve measurement robustness, and reduce pad count.

Yield Enhancement, and Production Process Control: Yield enhancement structures and methods, critical area calculation, defect estimation structures and methods, yield modeling, evaluation of design-manufacturing interactions, place and route methodology, and statistical process control. Large-scale, many-component test arrays and multiplexing techniques for technology assessment.

Test Structure Measurement Utilization Strategy: Test equipment, probing and programmable testing for process diagnostics, optimizing test throughput, database and data analysis methods, statistical data analysis, expert systems and related techniques, including capacitance, voltage, current, resistance, optical, and thermal measurements.

Matching and Variability Test Structures: Matching and variability of components (transistors, resistors, capacitors, inductors) and layout for circuit applications and their evaluation. Characterization of identically designed components. Modeling of matching and variability.

Authors are asked to submit an abstract of up to four pages in PDF format (font-embedded). The first page **must** consist of a title, a 50-words summary, author name(s), the full address, fax number, and e-mail address of the lead author, and author preference for oral or poster session presentation, if any. The body of the abstract should be three pages or less consisting of one page of text (800 to 1000 words) and up to two pages containing major figures and tables. Please visit the ICMTS 2015 official web site icmts2015.pdf.com for further information and paper submission. You may care to join the ICMTS group at www.linkedin.com.

The selection process will be based on the technical merit and will be highly weighted in favor of papers that have a high test structure content, include measured data and analysis, together with illustrations of the test structures involved. The submission deadline is **October 17, 2014**. Notice of paper acceptance, with instructions for manuscript preparation for the conference proceedings, will be sent to the authors of the papers selected for presentation by early December, 2014. The deadline for submission of the final paper will be January 20, 2015.

Details of the venue, hotel, registration, etc. will be posted at icmts2015.pdf.com as they are finalized.

For further technical information, please contact the technical chair:

Colin McAndrew, Freescale Semiconductor, Inc., Colin.McAndrew@freescale.com



International Microwave Symposium
IEEE 17 – 22 May 2015 Phoenix, AZ MTT-S



Come join us in Phoenix and enjoy the flagship Microwave Theory and Techniques Society (MTT-S) Conference

The IEEE Microwave Theory and Techniques Society's 2015 International Microwave Symposium (IMS2015) will be held **17 - 22 May 2015** at the Convention Center in Phoenix, Arizona as the centerpiece of Microwave Week 2015. IMS2015 offers technical sessions, interactive forums, plenary and panel sessions, workshops, short courses, industrial exhibits, application seminars, historical exhibits, and a wide variety of other technical and social activities including a guest program. As usual, the Microwave Week 2015 technical program also comprises the RFIC symposium (www.rfic2015.org) and the ARFTG conference (www.arftg.org). Unique to Microwave Week in 2015, a High School Science and Engineering Invitational will be held.

With over 9,000 participants and 800 industrial exhibits of state-of-the-art microwave products, Microwave Week is the world's largest gathering of Radio Frequency (RF) and microwave professionals and the most important forum for the latest research advances in the field.

Call for Papers

Authors are invited to submit technical papers describing original work on Radio-Frequency, microwave, millimeter-wave, and terahertz (THz) theory and techniques. The deadline for submission is **8 December 2014**. Please refer to the IMS2015 website (www.ims2015.org) for detailed instructions concerning paper submission. **Please don't wait** until the last day to start using the paper submission process. Those unfamiliar with the process may encounter paper formatting or clearance issues that may take time to resolve. The Symposium proceedings will be recorded on electronic media and archived in IEEE-Xplore. Authors of accepted papers should consider submitting an extended version of their symposium paper for possible publication in the *IEEE Transactions on Microwave Theory and Techniques*.

Emerging technical areas:

IMS2015 enthusiastically invites submission of papers that report state-of-the-art progress in technical areas that are outside the scope of those specifically listed in this Call for Papers, or that may be new to the symposium, but are of interest to our attendees.

Student paper and student design competitions:

Eligible students are encouraged to submit papers for the student paper competitions. In addition, eligible students or student teams are invited to consider taking part in student design competitions during the IMS2015, which are organized and sponsored by various Technical Committees (TC) of the MTT-S Technical Coordination Committee (TCC). Please visit the IMS2015 web site for full details.

MicroApps:

The Microwave Application Seminars serve as a forum for exhibitors at the IMS to present the technology behind their commercial products and their special capabilities. The presentations are open to all conference and exhibit attendees. All submissions must be made through email to microapps@ims2015.org. Please refer to the IMS2015 website for more information on submitting MicroApps technical papers.

We are looking forward to meeting you in Phoenix and sharing some of the hottest new microwave ideas under the sun!


IEEE

THE INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, INC.


 IEEE COMPONENTS, PACKAGING AND
MANUFACTURING TECHNOLOGY SOCIETY

IEEE Components, Packaging and Manufacturing Technology Society Phoenix Chapter

2014 Executive Committee for CPMT Chapter for IEEE-Phoenix Section

Position	Name	Phone Contact	Email Contact
Chair	Dr. Mahesh K. Shah	(480) 544-9438	mkshah@ieee.org
Asst. Chair	Mr. Vivek Gupta	(480) 734-2366	vmgupta@msn.com
Secretary	Dr. Devarajan Balaraman	(480) 619-0944	iangoinbiking@gmail.com
Treasurer	Dr. Vasudeva P. Atluri	(480) 227-8411	vpatluri@ieee.org
Program Chair	Mr. David Dougherty	(480) 245-8099	david.dougherty@freescale.com
Tutorial Chair	Dr. Ashish Gupta	(480) 554-2409	ashish.x.gupta@intel.com
Asst. Tutorial Chair	Adel Elsherbini	(734) 686-2278	a.elsherbini@gmail.com
Workshop Chair & Publicity	Dr. Vasudeva P. Atluri	(480) 227-8411	vpatluri@ieee.org
Website Co-Chair	Huiyang Fei		Huiyang.H.Fei@ieee.org
Website Co-Chair	Bharat Penmecha	(480) 552 2511	bharat.penmecha@ieee.org

Tentative Schedule for Monthly Seminars

We are working to arrange monthly Seminars on topics of interest to our members. If you have suggestion for topics and/or speakers please contact any of the executive committee members listed above.

November 19 – 3D Printing for the Electronics Industry, Jonathan Wright, PADT

Additional Activities –Tutorial and Workshop

Phoenix section is planning to hold a Half Day tutorial on the topic of **Reliability Engineering** in 2015. In addition we are working with other Society Chapters to hold a workshop on **Emerging Device and Packaging Technology** also in Spring 2015. Please wait for announcements in near future.

2015 Executive Committee

Please look for announcement for 2015 Executive Committee positions as well as election of officers if necessary in near future. The announcement will be sent out via email to all CPMT Chapter members on record.



THE INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, INC.



IEEE COMPONENTS, PACKAGING AND
MANUFACTURING TECHNOLOGY SOCIETY

IEEE Components, Packaging and Manufacturing Technology Society Phoenix Chapter

Wednesday, November 19th, 2014 at 5:30 PM

3D Printing for the Electronics Industry

Jonathon Wright

Applications Engineer

Phoenix Analysis Design Technologies

Tempe, Arizona

johnathon.wright@padtinc.com

ABSTRACT

The 3D Printing industry is currently estimated to be a \$3.8 billion per year industry for 2014 with estimated growth forecast of up to 45% per year over the next 5 years. This technology has the potential to fundamentally change the way we think about manufacturing. Some are even calling 3D Printing, the new Manufacturing Revolution. This presentation begins with an introduction of the different types of technologies that make up the 3D Printing world. We will discuss the many different industries where 3D Printing can have an incredible impact as well as the myriad of applications such as functional prototyping, conceptual modeling, injection molding, sand casting and a variety of other applications where 3D Printing truly shines. We will share how 3D Printing has been applied in the electronics industry and answer questions that attendees may have regarding 3D Printing technology and applications in this industry.

BIOGRAPHY

Jonathon Wright presently is an Applications Engineer at Phoenix Analysis and Design Technologies, Inc. (PADT). PADT is an engineering service company that focuses on helping customers who develop physical products by providing Numerical Simulation, Product Development, and Rapid Prototyping products and services. PADT's worldwide reputation for technical excellence and an experienced staff is based on its proven record of building long term win-win partnerships with vendors and customers. PADT has offices located in Arizona, Colorado, New Mexico, and Utah. PADT is distributor of 3D Printer and Direct Digital Manufacturing systems from Stratasys. PADT is also distributor of ANSYS® Inc. electro-mechanical simulation software. And, PADT designs and distributes CUBE, a line of high performance computing systems. In 2012, PADT was recognized by the Phoenix Section of the Institute of Electrical and Electronics Engineers (IEEE) for the "Small Company of the Year Technical Contributions Award". Mr. Wright graduated from Arizona State University (ASU) with a Bachelor of Science in Mechanical Engineering and will earn his Masters of Engineering Management from ASU in December. He is in his third year at PADT and specializes in Additive Manufacturing Technology.

Date: Wednesday, November 19th, 2014

Location: Group Conference Room, Freescale Semiconductor, Inc., 2100 E. Elliot Rd. Tempe, AZ. There are new signs on the property (Discovery Business Center). Enter the facility through the Main (South) Lobby in building 94 and sign in with Security (*Photo ID required*). You will be escorted to the meeting room.

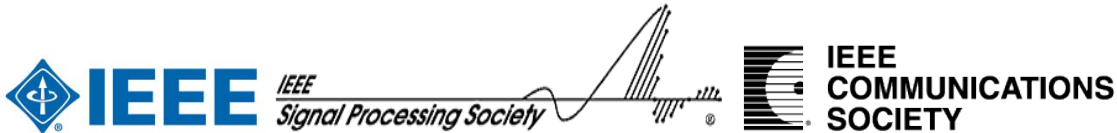
Agenda: 5:30–6:00 PM: Social/Refreshments, 6:00–7:00 PM: Presentation, 7:00 PM: Dinner
(Pizza and Soda will be provided by the IEEE Phoenix Section CPMT Society Chapter)

IEEE members and non-members are all welcome to attend.

Please arrive at the facility entrance no later than 5:45 PM.

For more information, please contact any of the following CPMT officers:

Vasu Atluri	(480) 227-8411	Devrajan Balaraman	(480) 619-0944	David Dougherty	(480) 413-6923
Adel Elsherbini	(734) 686-2278	Ashish Gupta	(480) 554-2409	Vivek Gupta	(480) 734-0266
Mahesh Shah	(480) 544-9438	Huiyang Fei		Bharat Penmecha	(480) 552-2511



SP-COM Phoenix Chapter

Please join our Google Group!

We have recently started a google group to be able to send you more timely announcements via email for upcoming events and talks. Please sign up for timely email announcements at the below link

<https://groups.google.com/d/forum/ieee-sp-com-phoenix-chapter>

We are limiting member permissions only to receiving emails posted by the group owner - the SP-COM chapter chair.

Technical Co-Sponsorship by the IEEE Signal Processing and Communications Chapter, Phoenix Section

We continue to post meeting notices on IEEE vtools at (<https://meetings.vtools.ieee.org/main>)



Phoenix Chapter of the IEEE Computer Society

November, 2014

News

- We are actively hunting for a new meeting venue. Our November meeting at DeVry will be the last meeting at that location. If anyone reading this has a suggestion for a venue for our meetings, please contact one of the officers. We need a room which will accommodate about 50 people, is available from 6:00 pm to 9:00 pm, has projection facilities, and is, hopefully, free. We are a 501(c)(3) organization.

Future Events

- Our November meeting will be held at DeVry on November 19th. **Note that this date is two weeks later than our normal meeting date.** The change is to accommodate the speaker for this meeting. Dr. Hal Berghel, our presenter in March, will return to provide us a presentation on "The Future of Digital Money Laundering".

This talk investigates several types of digital money laundering, characterized by source (failed states, state-aware, kleptocratic states, terrorists, extremists, and individuals), means (credit- and debit-card exploits, international funds transfers, klepto-banks, "gift-card" exploits), and purpose (terrorism, narco-trafficking, electronic crime, internet fraud). These categories are introduced by their identifying events-of-interest. Implications on shadow economies, degrees of sophistication, and case studies are discussed. Each crime will be explicitly linked geographically and politically to sources, and may include discussion of actual cases. Several micro- and macro-level mitigation strategies will be discussed.

Hal Berghel is currently Professor of Computer Science at the University of Nevada, Las Vegas where he has previously served as Director of both the School of Computer Science and School of Informatics, and Associate Dean of the College of Engineering. He is also the founding Director of the Identity Theft and Financial Fraud Research and Operations Center and CyberSecurity Research Center. His research interests are wide-ranging within the binary and digital ecosystem, ranging from logic programming and expert systems, relational database design, algorithms for non-resolution based inferencing, approximate string matching, digital watermarking and steganography, and digital security and privacy. Since the mid-1990's he has applied his work in digital security to law enforcement and intelligence gathering, particularly with respect to digital crime, digital money laundering, information warfare and trusted identities. His research has been supported by both industry and government for over thirty years. His most recent work in secure credentialing technology was funded by the Department of Justice. In addition to his academic positions, Berghel is also a popular columnist, author, frequent, talk show guest, inventor, and keynote speaker. For nearly fifteen years he wrote the popular Digital Village column for the Communications of the ACM, and has written the Out-of-Band column for IEEE Computer since 2011.

Berghel is a Fellow of both the Institute for Electrical and Electronics Engineers and the Association for Computing Machinery, and serves both societies as a Distinguished Visitor and Distinguished Lecturer,

respectively. He has received the ACM Outstanding Lecturer of the Year Award four times and was recognized for Lifetime Achievement in 2004. He has also received both the ACM Outstanding Contribution and Distinguished Service awards. He is also the founder and owner of Berghel.Net, a consultancy serving government, business and industry. Berghel is a member of the Nevada Technology Crimes Advisory Board and Chairs the Nevada Privacy Subcommittee.

- At the November meeting we will hold chapter elections for the 2015 calendar year. Anyone interesting in standing for any chapter office should contact one of the individuals listed below.

Meetings start at 6:00 pm with networking and light refreshments followed by the presentation at 7:00 pm. DeVry University is located at 2149 W Dunlap Avenue, Phoenix, (a mile east of I-17 on Dunlap).

Visit the CS Chapter website for the latest information: <http://ewh.ieee.org/r6/phoenix/compsociety/>.

For brief announcements regarding upcoming events we are also on Twitter: @IEEECS_PHX

If you would like to suggest a topic or speaker for any of our future meetings, please contact one of the chapter officers:

Chair:	Jerry Crow (jerry.crow@computer.org)
Vice-chair:	Brad Morantz (bradscientist@ieee.org)
Secretary/Webmaster:	Audrey Skidmore (askidmore@computer.org)
Treasurer:	Diane Smith (sdianesmith@computer.org)



IEEE Power and Energy Society
Phoenix Chapter

<http://www.ewh.ieee.org/soc/pes/phoenix/>



November 2014 Luncheon Meeting

Date: Thursday, November 20, 2014

Time: 11:30 - 11:45 am: Registration
11:45 am: Lunch
12:00 pm: Program

Location: SRP PERA Club ([map](#))
1 E Continental Dr
Tempe, AZ

Speaker: Dr. Ravi Gorur, Arizona State University

Topic: Dielectric research at ASU and its impact on power delivery

Cost: \$5.00 (No cost if you are a college student)

Reservations: Contact Gorman Company at (602) 470-0400 or submit your name [here](#). Reservations deadline is NOON on Monday, November 17, 2014. *If you have already registered for this luncheon but need to cancel, click [here](#).*

Abstract:

This talk will present highlights of research activity conducted at ASU on the subject of dielectrics or electrical insulation. The primary focus has been on overhead insulation on lines and stations. Line and apparatus insulators made from porcelain, toughened glass and composite materials have been investigated for better understanding failure modes, optimizing maintenance and innovative designs. Research has been mostly experimental and aided by substantial modeling with commercial and in-house computer programs. Recent efforts have focussed on composite materials used in high temperature low sag (HTLS) conductors and nanomaterials. The talk should be of interest to utility engineers and students who wish to pursue a research on hardware used in power delivery.

Biography:

Dr. Ravi Gorur has been with ASU since 1987. He has coauthored a textbook and over 200 papers in IEEE transactions and conference on the subject of insulators. He served as the US Representative to CIGRE Study Committee D1 (advanced materials and systems), chaired various technical committees in the IEEE that was responsible for the development of technical standards and state-of-the-art papers in this field. His most recent assignment (May 2013-May 2014) was as the Deputy Assistant Secretary of the Power Systems Engineering R&D in the US Department of Energy's Office of Electricity Delivery and Energy Reliability. He was elected Fellow of the IEEE in 1998 for contributions in the field of polymeric materials for outdoor insulation. He is passionate about undergraduate teaching, cricket (game, not the bug) and golf.

57th ANNUAL IEEE-PES FUNDRAISING GOLF TOURNAMENT



NOVEMBER 15, 2014

Be sure to join us at Rio Verde Golf Club, located at 18731 E 4 Peaks Blvd, Rio Verde, AZ 85263, for a 12:45 PM Shotgun Start. The course is located 10 Minutes north of Fountain Hills. (480) 471-9420 <http://www.rioverdecc.com/Map>

This is the annual fundraiser for your IEEE-PES Chapter. The fee is \$100 per player or \$400 to sponsor a foursome. Be sure to come early and stay late as there will be free range balls beforehand and dinner afterwards. Cash prizes awarded for first place, second place, and third to last place teams. There will also be a Raffle, Skills Games, and Giveaways Galore.

Use this sheet to sign up either individually or for your team. You can either:

- **Print out and complete, then mail the form with your payment**
- **Return this form electronically (distribution@gorman-co.com) and then send your payment.**
- **Send payments to: IEEE-PES
c/o Gorman Company
4819 35th Street
Phoenix AZ 85040**

You will be notified via email that your payment has been received and you are registered to participate. If you have any questions, please feel free to call us at (602) 470-0400.

Please provide all requested information, including email addresses for all team members.

TEAM/SPONSOR _____

Player _____ **Dinner?** _____

Email _____

Player _____ **Dinner?** _____

Email _____

Player _____ **Dinner?** _____

Email _____

Player _____ **Dinner?** _____

Email _____

Additional Dinner Guests: _____

To minimize waste and extra costs, we would like your help getting an accurate dinner count.



IEEE Reliability Society Arizona Chapter

The Reliability Society has just formed an Arizona chapter! This is a joint venture between the Ft. Huachuca, Tucson, and Phoenix Sections with the first event taking place this December. Please contact Phoenix Section Chair Bruce Ladewig (bruceladewig@ieee.org) if you are interested in attending.



IEEE SOLID-STATE
CIRCUITS SOCIETY

Your first engineering job

What you didn't learn in engineering school - Tips and pointers for navigating your way through your first few years

Students and young professionals are invited to join a discussion on how to be successful - pointers and lessons learned from engineers ranging from 2years - 30years of experience.

(Anyone interested in being part of the panel of experienced engineers, please email Mirembe@ieee.org by 11/4)

Place: ASU Goldwater 4th Floor: GWC 487

Date: **Friday November 7th**

Time: 12noon-1pm

Pizza and Drinks will be provided



INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS

WAVES AND DEVICES

PHOENIX CHAPTER

<http://ewh.ieee.org/r6/phoenix/wad/>*2014 Calendar*

<u>Date</u>	<u>Location</u>	<u>Topic / Title</u>	<u>Speaker</u>	<u>Affiliation</u>	<u>IEEE Society</u>
29-Jan	ASU Brickyard	Expanding your knowledge of Laser Fusion and Optical Accelerators	Ken Barat	Consultant	Photonics
6-Mar	ASU Goldwater	Embrace Circuit Nonlinearity to get Transmitter Linearity and Energy Efficiency	Earl McCune	MTT DML	MTTS
4-Apr	Agilent	Current and Future Trends in Photovoltaics Technologies	Christina Honsburg	ASU	EDS
25-Apr	Freescale	Bio Medical Devices	Jennifer Christen	ASU	EDSs
1-May	ASU	Software Defined Radio	Hossein Hashemi	USC	MTTS/ EDS
22-May	ASU	Antennas	Kathleen L. Melde	UofA	Antennas
Sept 24	Freescale	How Really Smart Engineers Make Really Dumb Mistakes	Colin McAndrew	Freescale	MTTS/ EDS
Oct, 29	TBD	Choosing Circuit Materials for Millimeter Wave Applications	John Coonrod	Rogers Corp	MTTS
Nov, 19	ASU	Radomes and Rotor Blade Modulation	Ron Lavin	Boeing Corp.	Antennas

INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS
WAVES AND DEVICES - Phoenix Chapter

Meeting Free & Open to Non-IEEE Members

6:30 to 8:00PM, Wednesday, Oct 29, 2014

*Arizona State University
Goldwater Center, GWC487
650 E. Tyler Mall, Tempe, AZ*



Topics in Aircraft Electromagnetics: Rotor Blade Modulation and Radomes

Ron Lavin

Associate Technical Fellow, Boeing

Abstracts

Abstract 1: The rotational motion of a helicopter's rotor blades can interfere with communication and navigation signal reception by imposing an unwanted modulation upon the amplitude, phase, and frequency of affected signals. The physics of rotor modulation is complex but can be reliably predicted based on the "quasi-stationary" technique introduced by Professor Jean Van Bladel, and additional techniques developed by Professor Constantine Balanis and members of the Advanced Helicopter Electromagnetic consortium at Arizona State University. This talk will introduce rotor blade modulation physics and provide examples of modeling it for different antennas on a helicopter operating from HF through Ku band. Strategies for mitigating rotor blade modulation are also offered.

Abstract 2: A radome (from RADAR + Dome, coined circa 1944) is a dome or other structure protecting antenna equipment and made from an electromagnetically transparent material. Aircraft radomes come in a variety of shapes and sizes, and protect a variety of antenna types including weather radar, communications and navigation systems, and various sensors. Proper radome design minimizes unwanted electromagnetic effects such as attenuation, de-polarization, and reflection/refraction of signals, and provides bandpass characteristics appropriate for the application. A review of considerations in radome design is provided with practical examples.

Biography

Ron Lavin is an Associate Technical Fellow in Boeing and a member of IEEE since 1997. He has 20 years of aerospace experience and has held key roles on many satellite, aircraft, and helicopter programs. Currently, he leads a team of multi-disciplinary engineers and scientists in providing innovative and affordable electromagnetic products and services for government and commercial customers, and serves as the Boeing technical monitor for the Advanced Helicopter Electromagnetics consortium at Arizona State University. Prior to joining Boeing in 2001, he was an FAA Spectrum Management official in Chicago. He holds an MS in Computer Science from DePaul University and a BSEE degree from Florida International University.

For more information, contact:

Steve Rockwell (WAD Chapter Chair) (480) 241-9891 steve.rockwell@ieee.org

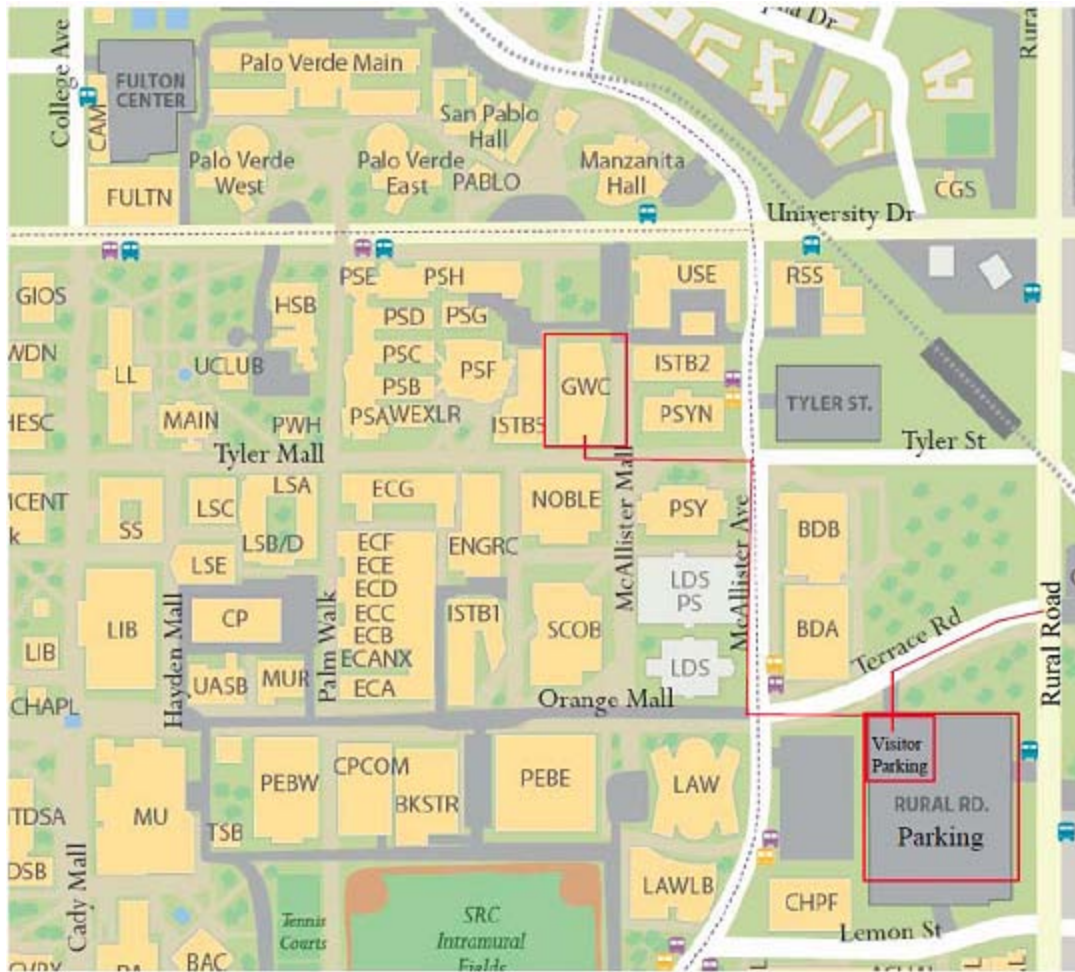
Curtis Scott (Chapter Publicity) (623) 703-9177 curtiscott@gmail.com

Date: Wednesday, Nov 19, 2014

Time: 6:30-8:00 PM Presentations (Pizza will be served following the Seminar)

Location: Goldwater Center, GWC487, Arizona State University, [650 E. Tyler Mall, Tempe, AZ](#)

Goldwater Center – GWC – Room 487
Building is across from Noble Science Library





**Technical, Elections, & Administrative
Meeting December 9, 2014**

Program Presentation: TBD

Meeting Agenda:

Attendee introductions

Program Presentation

Continued discussion of LMAG program for student assistance and possible scholarship program for HS students. Discuss results of visit to ASL by members.

Determine meeting dates for 2015.

Accept officer nominations and hold election of officers.

Administration Meeting.

Where: SRP's **PERA Club** Bighorn Room,
1 East Continental Drive, Tempe, AZ
Continental is West of 68th St., ½ mile south of McDowell Road
Enter the Private PERA Club and follow drive to large parking lot. Big Horn Room is the most South east building off parking.

When: Tuesday, December 9, 2014, 11:00am – 1:00pm, Registration fee is \$15. This fee will include lunch provided by the PERA Club. Lunch will be: BBQ Chicken salad, desert, Iced Tea, and Lemonade.

RSVP:

The Program Chair is seeking suggestion from members for future presentations. Any ideas of interest to LM are open for consideration. Please contact Ronald Sprague Program Chair at r.sprague@ieee.org or any officer with ideas.

About IEEE Phoenix Section Life Member Affinity Group:

The IEEE Phoenix Section Life Member Affinity Group was organized to enable IEEE Life Members to retain active IEEE associations, contribute to the social good in their communities, advance IEEE's professional interests and enjoy each other's company.

Activities: Technical meetings scheduled in February, May, October, and December. Elections are held at the December meeting.

Future Technical Meetings: All meeting are scheduled at the SRP PERA CLUB. It is suggested you put these dates on your calendar to attend our meetings.

- Tuesday, December 9, 2014

Officers:

Chair	Leslie Daviet II	lesdavietii@cs.com
Vice Chair	Position is Vacant	
Secretary	Tom Lundquist	Tom.Lundquist@ieee.org
Treasurer	Leslie Daviet II	lesdavietii@cs.com
Program	Ronald L. Sprague,	r.sprague@ieee.org
Past Chair	A. Barry Cummings	abarrycummings@gmail.com



IEEE Young Professionals (formerly GOLD) Phoenix Section Executive Board

- Chairman: Shafiul “Jacky” Islam (shafiul.islam@intel.com)
- Vice Chair: Jennifer Taggart (jennifer.taggart@asu.edu)
- Secretary/Webmaster: Joseph Caglio (joseph.m.caglio@intel.com)
- Treasurer: Ashley Kelly (Meredith) (ashley.kelly@aps.com)

October 3, 2014, 1st Annual Career Mixer with IEEE Phoenix Young Professionals, Women in Engineering, and IEEE / IEEE-HKN ASU groups

IEEE Phoenix Young Professionals in alliance with Women in Engineering and IEEE/IEEE-HKN ASU groups organized first annual career mixer for fundraising, networking, and recruitment.

When: October 3, 2014 4:30-7:30pm Where: Old Main @ ASU, 400 East Tyler Mall, Tempe, AZ 85287

For more information (including organizing committee) please visit: <https://meetings.vtools.ieee.org/m/28127>

We had sponsorships from APS, Chrysler, Electron International Incorporated, Intel Corporation, Kimley-Horn, and Texas Instruments. This event was very successful where we had about 180 attendees and many students were recruited. This was one of the largest event as part of IEEE GOLD (now Young Professionals) Strategic Alliance proposed by Shafiul “Jacky” Islam at Southwest Area Meeting on August 24, 2013 in Phoenix, Arizona. We look forward to continue as a strategic alliance to hold our upcoming events.



October 28, 2014, ASU Fulton Engineering Career Exploration Night for Freshman

Lesley Polka represented IEEE Young Professionals, and (along w/ Shamala) Women in Engineering and Phoenix Section at the event.

November 7, 2014, FE and PE Exams for IEEE Phoenix Young Professionals Affinity Group



IEEE Phoenix Young Professionals in alliance with Women in Engineering, and IEEE / IEEE-HKN ASU is organizing this technical talk / lecture by Barbara J. McMinn, P.E. (refreshments will be provided at the event).

When: November 7, 2014 6:00pm Where: GWC 487, 650 E. Tyler Mall, Tempe, AZ 85281

This presentation will provide basic information related to (FE and PE) application process and preparation strategies, and will, hopefully, enable the participants to develop preparation strategies that align with their needs.

Ms. McMinn is a Consulting Engineer, Arizona Public Service Company (APS), and a 29 year member of the Institute of Electrical and Electronic Engineers (IEEE). She was the 2014 Chair of the IEEE Phoenix Section

through July.

Registration: <https://meetings.vtools.ieee.org/m/30005>

Organizers: Shafiul “Jacky” Islam (shafiul.islam@intel.com)
Ashley Kelly (ashley.kelly@aps.com)



IEEE Phoenix Section
Women in Engineering
Affinity Group



Representatives of the IEEE Phoenix Section **Women in Engineering (WIE) Affiliate group (AG)** participated and involved in multiple activities during the month of October. The first event was in alliance with the IEEE Phoenix Young Professionals, WIE Engineers organized the **first annual career mixer** for recruiting, networking and fundraising on October 3rd in the ASU Campus. Engineers answered questions and gave tips to graduate students on how to prepare for interviews and internships. More details on this report can be viewed in the IEEE Young Professionals page.



Another Career mixer was held on October 28th, 2014, **ASU Fulton Engineering Career Exploration Night for freshman**. WIE representatives were present at the event interacting with freshman students and answering questions.



WIE in partnership with **Maricopa County Education Service Agency (MCESA)** organized two volunteering sessions for the STEM role model program which was held on October 10th and 17th in the Intel Chandler campus. The main mission for this event is to have STEM Professionals interact with middle school students through video conference and be Role Models, by sharing about themselves and what they currently do in their position, promoting STEM Identity. A huge turnout of Engineers came forward to volunteer for this role model Program



There is a line of interesting events that are coming in the month of November.

- WIE professionals will be judging the “Future City” contest organized by Dibble Engineering.
- Women in Engineering in alliance with IEEE Phoenix Young Professionals and IEEE / IEEE-HKN ASU is organizing a technical talk / lecture by Barbara J. McMinn, P.E

If you would be interested in helping to organize any of our activities or have suggestions for other activities or speakers for future events, please feel free to contact any of our Executive Committee members:

- Chair: Shamala Chickamenahalli (Shamala.Chickamenahalli@intel.com)
- Vice-Chair: Lesley Polka (lesley.a.polka@intel.com)
- Secretary / Publicity: Divya Kalimuthu (divya.kalimuthu@intel.com)
- Treasurer: Diane Smith (diane@web-oasis.com)
- Web Master: Audrey Skidmore (ASkidmore@azmag.gov)
- Membership: Lesley Polka (lesley.a.polka@intel.com)
- Region 6 Liaison: Barbara McMinn (barbara.mcminn@aps.com)

If you would like to be added to our distribution list for further information about this and future events, please contact Divya Kalimuthu (divya.kalimuthu@intel.com).

The IEEE WIE Affinity Group’s mission is to inspire, engage, encourage and empower IEEE women worldwide with a vision of creating a community of IEEE women and men innovating the world of tomorrow. More information about IEEE WIE can be found at: http://www.ieee.org/membership_services/membership/women/women_about.html



2014 IEEE Phoenix Section News

Executive Committee Meeting

No meeting of Executive Committee in July & August

Normal meetings are on first Tuesday of the month from 6:00 PM to 8:00 PM
The Airport Hilton Phoenix,
2435 S 47th St. Phoenix, AZ 85034, (480) 894-1600.

2014 Executive Committee

Chair: Bruce Ladewig
Vice Chair: Bruce Ladewig
Secretary: Surinder Tuli
Treasurer: Vivek Gupta
Past Chair: Charles Weitzel

Executive Committee Meetings

Date: First Tuesday of every month, except July and August

Time: 6:00 – 8:00 p.m.

Location: Hilton Phoenix Airport, 2435 South 47th Street, Phoenix, AZ 85034

IEEE Phoenix Section Annual Banquet 2014 Photos are Posted at:

<http://sites.ieee.org/phoenix/2014/03/03/pictures-from-the-2014-annual-banquet/>

IEEE Senior Member and Fellow Grade

IEEE Phoenix Section Membership Development would like to nominate eligible IEEE Members from the Section to Senior Member and Fellow Grades. Please review the requirements at www.ieee.org for eligibility.

Eligible candidates are requested to send in their resumes to Dr. Vasudeva P. Atluri, Membership Development Coordinator, at vpatluri@ieee.org and Dr. Charles E. Weitzel, Section Chair, at c.weitzel@ieee.org for consideration.



IEEE Phoenix Section Nomination of Officers

The IEEE Phoenix Section is seeking nominations for the following elected Section officer positions for the 2015 term:

- Chair
- Vice Chair
- Secretary
- Treasurer

Additionally, the Nominating Committee is seeking candidates for some of the following non-elected appointed standing committee Chair positions for the 2015 term:

- Publicity
- Membership
- Student Activities
- Conferences
- Awards
- Inter-Society
- PACE (Professional Activities Committees for Engineers)
- TISP (Teacher In-Service Program)
- Web Master

Section officers must be IEEE members. Self-nominations are acceptable.

Please send nominations or any questions to any member of the Nomination Committee:

Chuck Weitzel (Chair): c.weitzel@cox.net
Trevor Thornton: t.thornton@asu.edu
Les Daviet II: lesdavietii@cs.com

Deadline for Nominations is December 1, 2014.

Section Officer Job Descriptions

Section Chair

The Section Chair shall serve as Chair for all meetings of the Section, the Section Committee and the Section ExCom where such ExCom exists.

Roles:

- Preside at meetings of the Section Executive Committee
- Represent the Section at IEEE gatherings
- Represent the Section at Regional Committee Meetings and vote on issues on the basis of the best interest of IEEE

Vice Chair

The Section Vice Chair shall assist the Chair in the coordination of all Section activities. At the request of the Section Chair or in the absence of the Section Chair, the Section Vice Chair shall chair meetings of the Section, Section Committee or Section ExCom.

Secretary

The duties of the Section Secretary shall include correspondence, the keeping of the minutes of the Committee meetings, mailing notices, and submission of meeting and officer reports to the Member and Geographic Activities Department at the end of each year and such other duties as are assigned to him/her by the Chair.

Treasurer

The duties of the Section Treasurer shall include the development of a Section budget for approval by the Section, accounting of all Section funds, keeping financial records, and submitting the Financial Operations Report of the Section to the IEEE Staff Director, Financial Services.

Publicity

- Publish and distribute monthly section newsletter
- Maintain section email lists
- Support chapter publicity submissions to the the IEEE E-Notice system

Membership Chair

- Monitoring a current record of membership.
- Analyzing membership trends.
- Ensure adequate supplies of membership development materials available for distribution (chapter meetings, conferences, job fairs, etc.).
- Coordinating membership exhibits for local meetings and conferences, soliciting materials for exhibits, indentifying volunteers to staff booth.

Student Activities Chair

- Coordinate and support student chapters by working with the student chapter representatives and their faculty advisers.
- Organize the annual student paper competition
- Organize the annual student scholarship awards program

Conferences Chair

- Support and act as liaison between the section and locally held IEEE affiliated conferences
- Maintain calendar of IEEE related conference events on the section website calendar

Awards Chair

- Plan, coordinate, and organize the annual awards banquet

Inter-Society Chair

- Coordinate and act as liaison between section and other engineering or relevant organizational entities

PACE (Professional Activities Committees for Engineers) Chair

- Coordinate and support professional and career developmental activities

TISP (Teacher In-Service Program) Chair

- Coordinate the local TISP program

Web Master Chair

- Maintain and update the Section website as needed

IEEE Phoenix Section Awards

The IEEE Phoenix Section recognizes the contributions of members, non-members, society chapters, affinity groups, student branches, corporations, and educational institutions at the annual banquet. The next banquet is scheduled for Saturday, February 7th, 2015 at Hilton Phoenix Airport located at 2435 South 47th Street, Phoenix, AZ – 85034 from 5:30 PM to 9:30 PM (Tel: 480-894-1600).

The Section is pleased to issue a call for nominations for this year's Section awards. The scope and purpose of the Section Awards Program is to plan, promote and implement IEEE award programs that recognize outstanding performance in furthering the objectives and professional aims of the IEEE Phoenix Section, the IEEE, and the IEEE-USA, and to stimulate others to pursue such achievements of excellence.

The Phoenix Section has established the following general award categories:

- Member
- Society Chapter/Affinity Group
- Non-member
- Corporate
- Educational
- Section Chair

The Awards Guide lists the awards along with the selection criteria that will be implemented for selecting the award recipient. The Awards Guide can be accessed at <http://sites.ieee.org/phoenix>.

Please read through the Awards Guide to help you in selecting the award category for nomination.

All sections of the form should be completely filled electronically. Submission of additional documents such as resume in support of the nomination is highly encouraged.

Key dates:

Submission deadline: Sunday, January 4th, 2015

Awardees and nominators will be informed on: Sunday, January 11th, 2015

Only nomination forms submitted by the deadline will be reviewed by the awards committee consisting of the following IEEE Phoenix Section Officers:

- Awards Committee Chair: Dr. Vasudeva P. Atluri
- Past Chair: Dr. Charles E. Weitzel
- Chair (Interim) / Vice-Chair: Mr. Bruce J. Ladewig
- Secretary: Mr. Surinder K. Tuli
- Students Activities Coordinator: Dr. S. Diane Smith

If you have any additional questions, please contact Dr. Vasudeva P. Atluri, Section Awards Committee Chair, at (480) 227-8411 or by email at vpatluri@ieee.org, and Mr. Bruce J. Ladewig, Section Chair, at (480) 620-9291 or by email at bruceladewig@ieee.org.

IEEE Phoenix Section Student Scholarships

The IEEE Phoenix Section awards Student Scholarships to full-time graduate and undergraduate students who are IEEE student members. The applying student must attend a university in the Phoenix Section during 2014. The universities include Arizona State University, Arizona State University Polytechnic, DeVry University, Embry-Riddle Aeronautical University, and Northern Arizona University.

The student should submit a completely filled application along with financial aid statement; a one-page personal statement of achievements, interests, and goals; official transcripts of all college work; and recommendation letters. The scholarships are awarded based on academic achievement, financial need, and service to the IEEE.

Student scholarship winners, upon receiving notification from IEEE Phoenix Section Awards Committee, should send by email completely filled either Form W-9 for US Citizen or Form W-8 for Non-US Resident to Mr. Michael Deering, Director of Development, IEEE Foundation, at m.deering@ieee.org with a copy to Dr. Vasudeva P. Atluri, Section Award Committee Chair, at vpatluri@ieee.org by Friday, January 2nd, 2015.

The IEEE Phoenix Section will present the following Student Scholarship awards at the annual banquet scheduled for Saturday, February 7th, 2015 at Hilton Phoenix Airport located at 2435 South 47th Street, Phoenix, AZ – 85034 from 5:30 PM to 9:30 PM (Tel: 480-894-1600):

“Atluri Award” sponsored by Dr. Vasudeva Prasad Atluri and Dr. Satyavathi Atluri. Total of two award recipients are presented with a plaque and a check for \$1000.00 each. Only graduate students are eligible for these awards.

“AI Gross Award” sponsored by IEEE Components, Packaging, and Manufacturing Technology (CPMT) Society Phoenix Chapter. The award recipient is presented with a plaque and a check for \$1000.00. Graduate and undergraduate students are eligible for this award.

“Irv Kaufman Award” sponsored by IEEE Waves and Devices Phoenix Chapter. The award recipient is presented with a plaque and a check for \$1000.00. Graduate and undergraduate students are eligible for this award.

“Dieter Schroder Award” sponsored by IEEE Phoenix Section. The award recipient is presented with a plaque and a check for \$1000.00. Graduate and undergraduate students are eligible for this award.

The checks for these four awards are given out from the IEEE Phoenix Section Student Scholarship Endowment administered by the IEEE Foundation. To learn more about IEEE Foundation, access <http://www.ieeefoundation.org/>.

Key dates:

Application submission deadline: Sunday, December 28th, 2014

Scholarship recipients will be informed on: Wednesday, December 31st, 2014

Phoenix Section LinkedIn Group


If you are interested in professional networking and shared Section related updates & discussions join the [IEEE Phoenix Section Group on LinkedIn](#). Signing up only takes minutes and is free. A job board is available as well.

You can also go to IEEE Phoenix Section LinkedIn page by clicking  button on the [IEEE Phoenix Section home page](#)

IEEE Phoenix Section Ventures into Social Media

You can access the web page three ways:

Use the URL: <https://www.facebook.com/IEEEPhoenixSection>

Click on the Facebook logo  link from [IEEE Phoenix section home page](#).

Search for IEEE Phoenix Section from your Facebook page.

We need following help.

1. Each of you access the IEEE Phoenix Section Web page and click on "Like" hyperlink.
2. Go on the Friends section of the page and "Invite Your Friends." Once you click on Invite button, it will get your email contact list. Your facebook contact list will already be populated with your Facebook friends and you can simply click the Invite button next to their name. Please invite as many friends as you can.
3. Provide me the contents for posting on a regular basis - meeting/ event announcements, Event pictures, Videos.
4. Start some discussion topics under - Status section.

IEEE Membership Grade Advancement

IEEE Phoenix Section Executive Committee encourages all to apply for advancement in membership grade to Senior Member and Fellow Grade. Please review the requirements at www.ieee.org. Please contact IEEE Phoenix Section Membership Development Chair, Dr. Vasudeva P. Atluri, at vpatluri@ieee.org for additional information.

Enhanced Senior Member Application Launched

Effective 29 July 2011, IEEE Admission and Advancement launched a [new Senior Member Application](#). The new application includes numerous enhancements, based on feedback from volunteers and members, including:

- New user friendly format / design
- Secure environment (need IEEE Web account)
- Ability to save application in “draft” form
- Ability to upload resume or Curriculum Vitae (up to 3 MB)
- Applicant can view application online
- Applicant can view status of requested reference forms
- References will be notified by email to provide applicant reference
- References will have the ability to view their completed reference form(s)
- Real time application status

The goal is to provide prospective Senior Members with an easy to use and intuitive interface, while streamlining internal operations at the same time. [View the new Senior Member application](#).

IEEE Member's Benefits

IEEE-USA Offering Survivor Planning E-book Free to IEEE Members

November Free E-Book

In November, IEEE-USA will offer “Writing for Success -- An Engineer’s Guide -- Volume 2: The Road to Excellence.”

Author Tom Moran shares that while it would be great to “let our fingers fly across the keyboard to rough out a report, run spell check, and send the results off to be printed or distributed, the finished product would mostly likely be filled with inaccuracies, be unclear to the readers, and ultimately -- ineffective.”

He points out that the road to writing excellence is filled with revisions. He emphasizes that writers must thoroughly examine what they write; ask questions to ensure their information is accurate, complete, logical and clear; and most importantly, that the information is necessary. Checking for spelling, punctuation and proper grammar is a must.

Moran writes that attention to detail takes a critical eye and that good writers test, write, rewrite and test again. Moran assures readers that following the process will gradually improve their writing and instill confidence “on the road to writing excellence.”

“Writing for Success -- An Engineer’s Guide -- Volume 2: The Road to Excellence” can be downloaded at <http://www.ieeeusa.org/communications/ebooks/files/dec14/83901nz/Writing-for-Success-Volume-2.pdf> for free to IEEE members. \$9.99 for Non-Members.

In December, IEEE-USA E-books will offer “Starting Your Start-Up – Book 2: Market Size & Strategy” by Tanya Candia.

Call for Authors

IEEE-USA E-books seek authors to write an e-book, or an e-book series, on career guidance and development topics. If you have an e-book idea that will benefit members on a particular topic of expertise, email your proposal to IEEE-USA Publishing Manager Georgia C. Stelluto at g.stelluto@ieee.org and IEEE-USA Communications Committee Chair Gus Gaynor at g.gaynor@ieee.org.

IEEE-USA serves the public good and promotes the careers and public policy interests of more than 200,000 engineering, computing and technology professionals who are U.S. members of IEEE.

Web: www.ieeeusa.org

Facebook: www.facebook.com/ieeeusa

Twitter: www.twitter.com/ieeeusa

Join IEEE: www.ieee.org/join

Contact: Sharon C. Richardson, Coordinator
IEEE-USA Communications & Publishing
Phone: 1 202 530 8363
E-mail: s.richardson@ieee.org

GoogleApps@IEEE Now Available to IEEE Members

GoogleApps@IEEE is a suite of products offered to IEEE members to enhance peer-to-peer communications and collaboration. The suite of applications includes e-mail, calendaring, contacts, and document sharing along with other collaborative tools. 30G of available cloud storage memory is available for these applications. Learn more about <http://www.ieee.org/googleapps>

GoogleApps@IEEE is available to members at no additional cost, bringing access to:

- A unique IEEE e-mail address (e.g., John.A.Doe@ieee.org);
- Mail forwarding or e-mail inbox (Gmail);
- 30-gigabytes of shared storage (Gmail and Google Drive);
- 99.9 percent up time guaranteed by Google;
- Advertisement-free Gmail;
- Files stored in the cloud for easy sharing and access from anywhere via Google Drive.